

Overview

Shikatronics' SSG-1G/ZX10 is a kit of two pieces of 64M x 72-bit (512MB) PC133 CL3 (CAS Latency 3) Registered SDRAM (Synchronous DRAM) memory modules. The components on this kit include eighteen 64M x 4-bit SDRAM PC133 components in TSOP packages each module. This kit of 168-pin DIMM uses gold contact fingers and requires + 3.3V. The electrical and mechanical specifications are follows:

Features

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|--------------------------|--|
| ⌚ CLK Cycle Time (tCK) | min. = 7.5ns (CAS Latency = 3) / max. = 1000ns |
| ⌚ Row Cycle Time (tRC) | 65ns |
| ⌚ CLK to Output (tAC) | max. = 5.4ns (CAS Latency = 3) |
| ⌚ Single Power Supply of | +3.3V (+/- .3V) |
| ⌚ Power | 8.895 W (operating) |
| ⌚ UL Rating | 94 V - 0 |

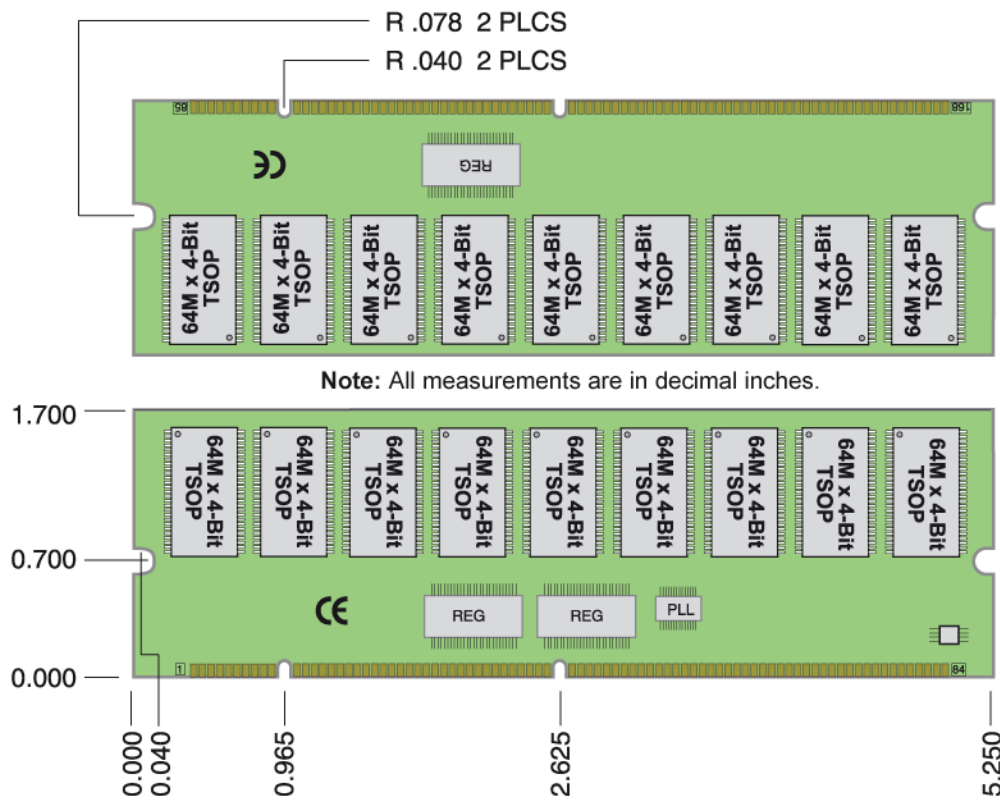


Figure 1: SSG-1G/ZX10 168-Pin SDRAM