

SBM-512M/33L55037

2 X 256MB REGISTERED DDR266 SDRAM 184-PIN ECC
Dual In Line Memory Module (DIMM)

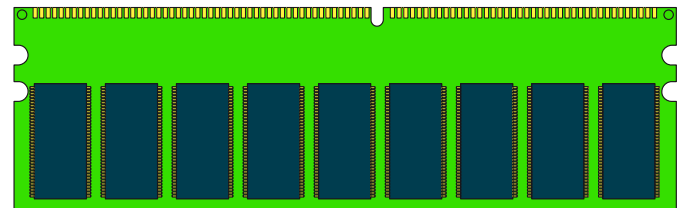
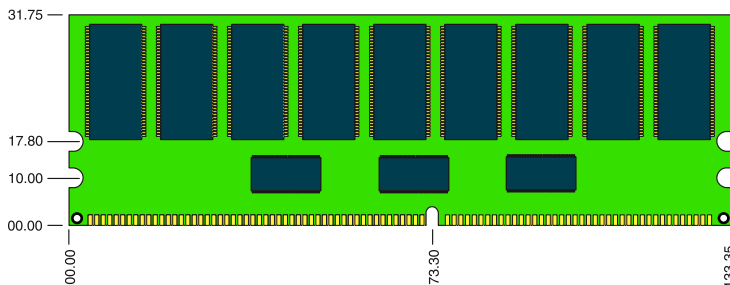


MEMORY MODULE SPECIFICATION

This document describes SBM-512M/33L55037 memory kit of two 256MB, CAS Latency 2.5 SDRAM (Synchronous DRAM) Registered DDR266 ECC memory modules. Total kit capacity is 512MB. The components on each module include eighteen 16M x 8-bit (4M x 8-bit x 4Bank / 133MHz, 7.5ns, CL2.5 components) SDRAM in TSOP packages and three 16-bit drivers in TSSOP packages. Each 184-pin DIMM uses gold contact fingers and requires +2.5V. The electrical and mechanical specifications are as follows:

FEATURES:

- ★ Clock Cycle Time (tCK) 7.5ns (min.) / 15ns (max.)
- ★ Row Cycle Time (tRC) 65ns (min.)
- ★ Refresh Row Cycle Time (tRFC) 75ns (min.)
- ★ Row Active Time (tRAS) 45ns (min.) / 12,000ns (max.)
- ★ Single Power Supply of +2.5V (+/- .25V)
- ★ Power 4.200 W (operating)
- ★ UL Rating 94 V - 0



Units : Millimeters

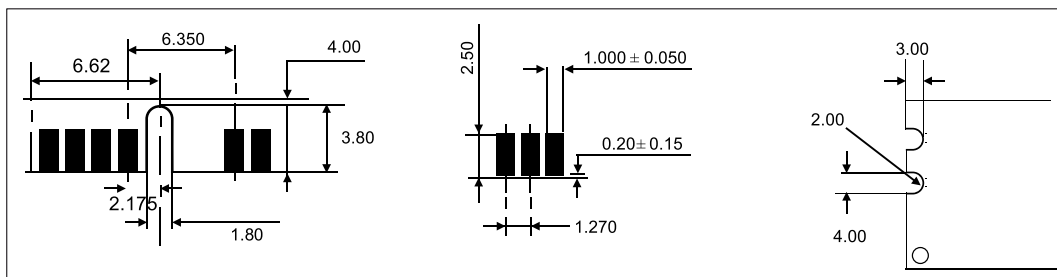


Figure: **SBM-512M/33L55037 (2 X256MB)**

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).